Two day workshop on Digital Signal Processing and Design of Controllers 12-13 August, 2016 Registration Form

Full Name:
Gender: Male / Female
Designation:
Organization:
Address:
Email:
Phone No.:
Accommodation required: Yes / No
Demand Draft Details: Faculty/Student
Amount: D.D.No.:
Bank:

Place: Date:

### **About VIT University**

Founded in 1984 by Dr. G. Viswanathan, as Vellore Engineering College, the institute was accorded a deemed university status in recognition of its academic excellence by Ministry of Human Resource Development, Government of India in June 2001. VIT University has made a mark in the field of higher education in India by imparting quality education in a cross cultural ambience combined with extensive application oriented research. VIT University ranked No.1 Private Engineering Institution by MHRD, Govt. of India (NIRF-2016 ranking).

### **About School of Electrical Engineering**

The School of Electrical Engineering (SELECT) has 82 faculty members who have done their UG and PG degrees from the top-notch universities. The faculty members of this school are consistently doing well in teaching and research. Faculty members and students of SELECT frequently get awards, prizes for outstanding research contributions in their respective fields. The School offers B.Tech (Electrical and Electronics Engineering), B.Tech (Electronics and Instrumentation Engineering), M.Tech (Power Electronics and Drives) and M. Tech (Control and Automation), M.Tech.by Research and Ph.D. in Engineering. All UG & PG programs of the school are accredited by the Institution of Engineering and Technology (IET), U.K. ABET (USA)



Two day workshop on Digital signal Processing and Design of Controllers

# 12-13 August, 2016

# Organized by

# **School of Electrical Engineering**



A Place to Learn, A Chance to Grow

Signature of the participant

## About the workshop

The human computer interface (HCI). automated systems based on images and signals are recent trends due to advancements in digital technology. The HCI developed based on signals helps in rehabilitation engineering for control of assistive devices, gesture control, etc. and automatic diagnostic system relives the medical practitioner in identification of subject condition from the experience. It is possible to develop such expert system, through the processing of signals acquired from the subject. Further, the controller is the heart of the automated systems. The design of controller is essential in proper functioning of automated system. The goals of this workshop to discuss on various transform techniques for processing of signals, extracting characteristics of the signals and recognition of the signal. Further, the design of controller with various control techniques for automated systems.

#### **Resource Persons**

**Dr. P. Geethanjali**, Associate Professor School of Electrical Engineering VIT University, Vellore.

**Shri B. Venkateshwar,** Add. General Manager Informatics Centre (R&D), BHEL/BAP, Ranipet, Vellore.

**Prof. A. Sharmila**, Assistant Professor School of Electrical Engineering VIT University, Vellore.

**Prof. V. Sankardoss**, Assistant Professor (Sr.) School of Electrical Engineering VIT University, Vellore.

## **Topics to be covered**

- Transform techniques and its importance
- Extraction of various features of the signals
- Dimensionality reduction through feature selection and feature reduction techniques
- Classification of signals
- Application of signal processing in medical diagnostic system, human computer interface
- Techniques in design of controller
- Design of controller with various tools
- $\succ$  Hands on session

Last date for Registration: On or before August 07, 2016

## **Registration fee**

Faculty/Engineers: Rs. 2000 Students/ Research scholars: Rs. 1500 (Registration fee includes 14.5% of tax)

Workshop kits, Workshop lunch and Snacks will be provided during the event. Certificate will be issued to all registered participants. Interested members are requested to fill up the registration form and send it to the following address with **Demand Draft drawn in favour of 'VIT University'** payable at Vellore, Tamil Nadu.

## **Advisory committee**

## **Chief patron**

**Dr. G. Viswanathan** Chancellor

### **Patrons**

Mr. SankarViswanathan, Vice President Dr. SekarViswanathan, Vice President Mr. G. V. Selvam, Vice President Dr. Anand A. Samuel, Vice-Chancellor Dr. S. Narayanan, Pro Vice-Chancellor Dr. V. Raju, Pro Vice-Chancellor

### Advisor

**Dr. P. Arulmozhivarman** Professor and Dean School of Electrical engineering VIT University, Vellore.

## **Organizing Secretary**

**Dr. P. Geethanjali** Associate Professor School of Electrical Engineering VIT University, Vellore.

### **Organizing Coordinators**

Prof. A. Sharmila & Prof. V. Sankardoss School of Electrical Engineering VIT University, Vellore. 9487556692, asharmila@vit.ac.in 9095174226, vsankardoss@vit.ac.in