OBJECTIVE

FPGAs have gone from being simple glue logic chips to actually replacing Custom Application-Specific Integrated Circuits (**ASIC's**) and processors for Digital systems, signal processing and control applications. The only deterrent which prevents many in exploring the FPGA way is the steep learning curve associated with HDLs. Xilinx Simulink toolkit targets to remove this by providing a set of libraries which can be easily used for majority of FPGA applications. Hence getting acquainted with the Xilinx tool chain is the way to go for any student or faculty looking to explore the world of FPGA based digital system design development.

This one day workshop in the area of Digital Systems and its applications in FPGA aims to enhance the intellectuals towards the design of digital circuits for real time applications. This workshop aims to play a vital role by focusing the technical issues dealing with digital logics and its applications. The Programming methods can be done in Verilog HDL or VHDL and applied through FPGA design flow tool box platform which will be **discussed** and **hands on training** will be provided along with various FPGA based implementation methods.

This workshop will help the Academicians, Professionals and Students to explore ways to harness more of digital circuit design logics in FPGA tool flow for real time applications.

FACULTY

Sessions will be handled by experts from various reputed Institutions and experts from Kongu Engineering College

COURSE CONTENTS

- VLSI Design Flow
- Introduction to Verilog & VHDL Programming.
- Design and Simulation of Digital Circuits using VHDL and Verilog
- Xilinx Tool Flow
- FPGA-Spartan 3E development kit
- Implementation of Digital Circuits using FPGA – Spartan 3E Kit
- Xilinx MATLAB Co-simulation using System Generator for Spartan & FPGA Kits

BOARDING AND LODGING

Boarding and lodging will be provided to the selected candidates in the college campus. Accommodation will be provided in the college hostel to the participants only on a chargeable basis. Participants are required to make their own arrangements for those accompanying them, if any.

REGISTRATION DETAILS

Faculty Members	Rs. 700/- *
Students	Rs. 500/- *

* Including Tax

Demand Draft for the registration fee payable at Perundurai must be drawn in favor of "IIP Cell Grant-in-aid A/C", Kongu Engineering College.

HOW TO APPLY

The applicants should send their applications in the specified format through their Principals / Sponsors to reach us on or before 21.03.2016. If selected, they should confirm their participation in time.

IMPORTANT DATES

Last Date for Receipt of Applications	: 21.03.2016
Date of intimation regarding selection	: 22.03.2016
Confirmation by Participants	: 23.03.2016

KONGU ENGINEERING COLLEGE
(*Autonomous*)
PERUNDURAI, ERODE 638052 TAMILNADU

NATIONAL WORKSHOP

on

"Digital System Design, Verification and Implementation Using FPGA"

24th March, 2016
(Thursday)

APPLICATION FORM

Name	:
Designation	:
Organization	:
Gender	:
Age	:
Educational Qualifications	:
Address for Communication	:
Mobile Number	:
E-mail ID	:
Experience	:
Teaching	: _____years
Others (Specify)	: _____years
DD Details	
DD. No.	:
Amount	:
Date	:
Bank Name	:
Need Accommodation	: YES / NO

DECLARATION

The above information is true to the best of my knowledge. I agree to abide by the rules and regulations governing the course. If selected, I shall attend the programme for the entire duration. I also undertake the responsibility to inform the Coordinator in case I am unable to attend the course.

Place:

Date: Signature of the Applicant

SPONSORSHIP CERTIFICATE

Mr/Ms/Dr_____

_____ is an employee of our Institute / Organization and is hereby sponsored. He / She will be permitted to attend the programme in full, if selected.

Place: Signature of the Sponsoring Authority

Date: Office Seal

Application form completed in all respects is to be sent to:

Dr.S.Maheswari M.E., Ph.D.,
Assistant Professor (Sr.G)/EEE
Coordinator
National Workshop on

"Digital System Design, Verification and Implementation Using FPGA"

Department of EEE
Kongu Engineering College
Perundurai, Erode-638 052, Tamilnadu.
Contact Mobile – 9750111244, 9443350335

ABOUT THE COLLEGE

Kongu Engineering College (KEC) established in the year 1984, approved by AICTE New Delhi and affiliated to Anna University, Chennai has completed 31 years of dedicated and excellent service in the field of technical education. The college offers 14 UG programme, 19 PG programme and 16 Research programme in Engineering and Applied Sciences. The institution has established a Technology Business Incubator (TBI) supported by Department of Science and Technology, Government of India in its campus. TBI @ KEC has won the National Award, presented by Shri. Pranab Mukherjee, President of India on Technology Day in New Delhi on 11th May, 2013. Best Engineering College award, Best Principal award and ranked 2nd position in Tamilnadu and 12th position among all engineering colleges in India in Competition Success review (July 2014).

ABOUT THE DEPARTMENT

The Electrical and Electronics Engineering department occupies a prominent place in the chronicles of its academic history. The department has been consistently producing illustrious Engineering graduates of high caliber who occupy prestigious positions in the academic and industrial fields. The specialization of the faculty includes Power System Engineering, Computer Networks, Power Electronics, Control Systems, Instrumentation and Control etc. The department offers four year UG programme in EEE and two year PG programme in Applied Electronics and Power Electronics and Drives. Department has bagged the National level award under the category **"Best Industry linked Technical Institute for the Electrical Engineering stream"**, on the survey organized jointly by AICTE and CII during June-July 2013, for the year 2012-13.

ABOUT THE LOCATION

The college is situated at Perundurai on the National Highway (NH 47) about 80 km from Coimbatore and 20 km from Erode.

Estd : 1984

Transform Yourself

NATIONAL WORKSHOP
on

Digital System Design, Verification and Implementation Using FPGA

24th March, 2016
(Thursday)

Coordinators

Dr.S.Maheswari
Assistant Professor (Sr.G)

Mr. S.K.Logesh
Assistant Professor

Ms.S.Aruna
Assistant Professor

Organized by

School of Electrical Sciences
Department of Electrical and Electronics Engg.
Kongu Engineering College
Perundurai – 638052 Erode TamilNadu

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KONGU
Assuring the Best